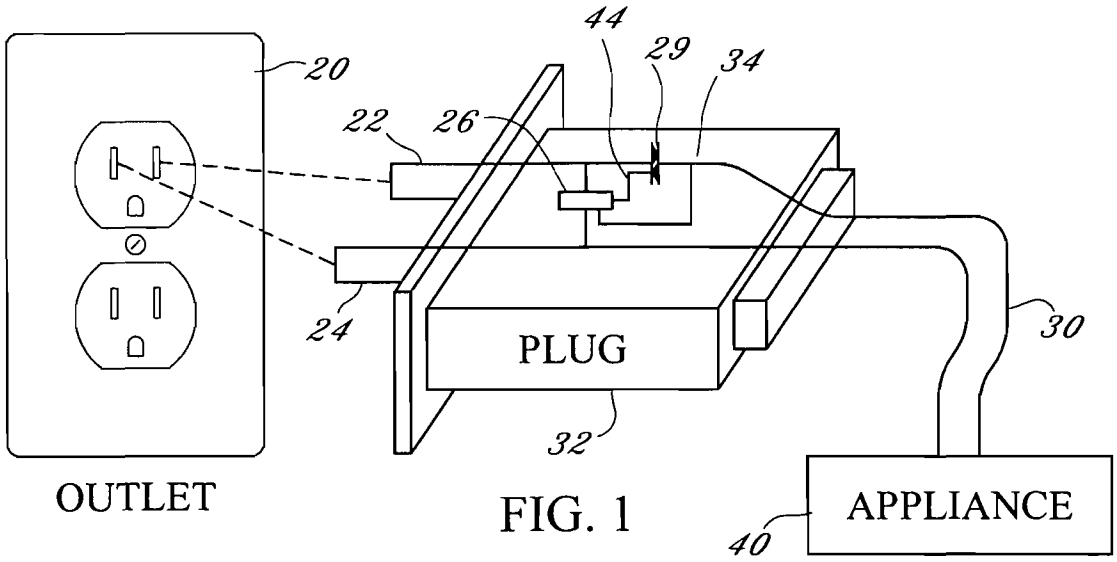

44
29
34
20
22
26
24
PLUG
30
32
OUTLET
APPLIANCE
40

FIG. 1

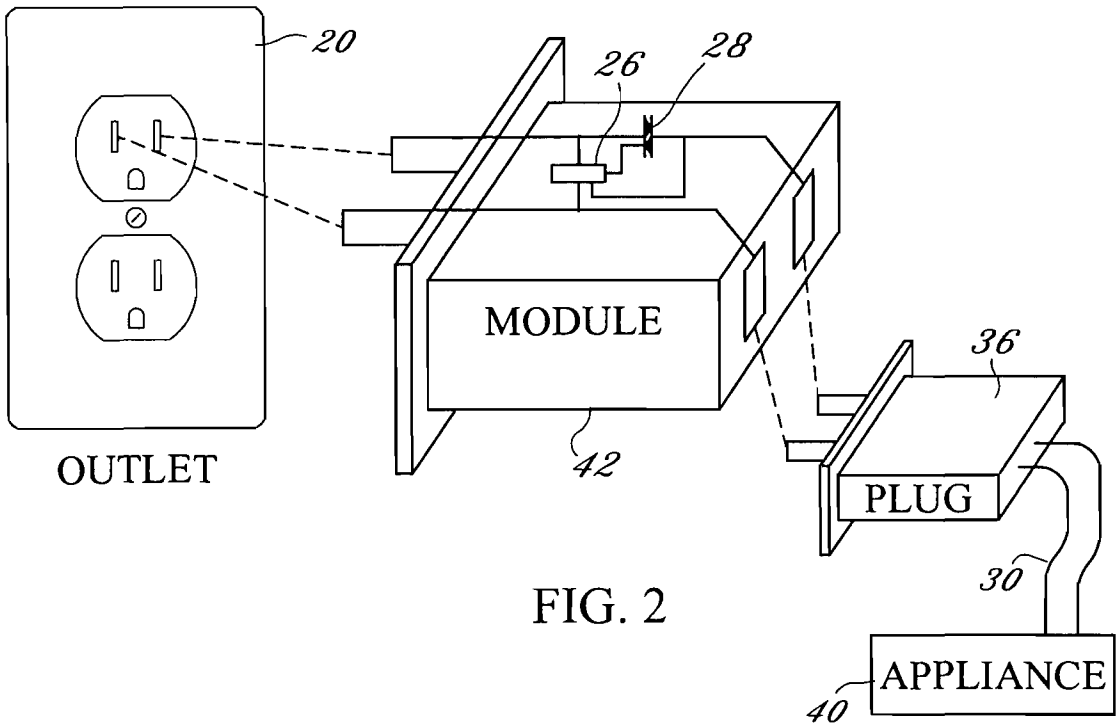

20
26
28
MODULE
36
42
PLUG
30
OUTLET
APPLIANCE
40

FIG. 2

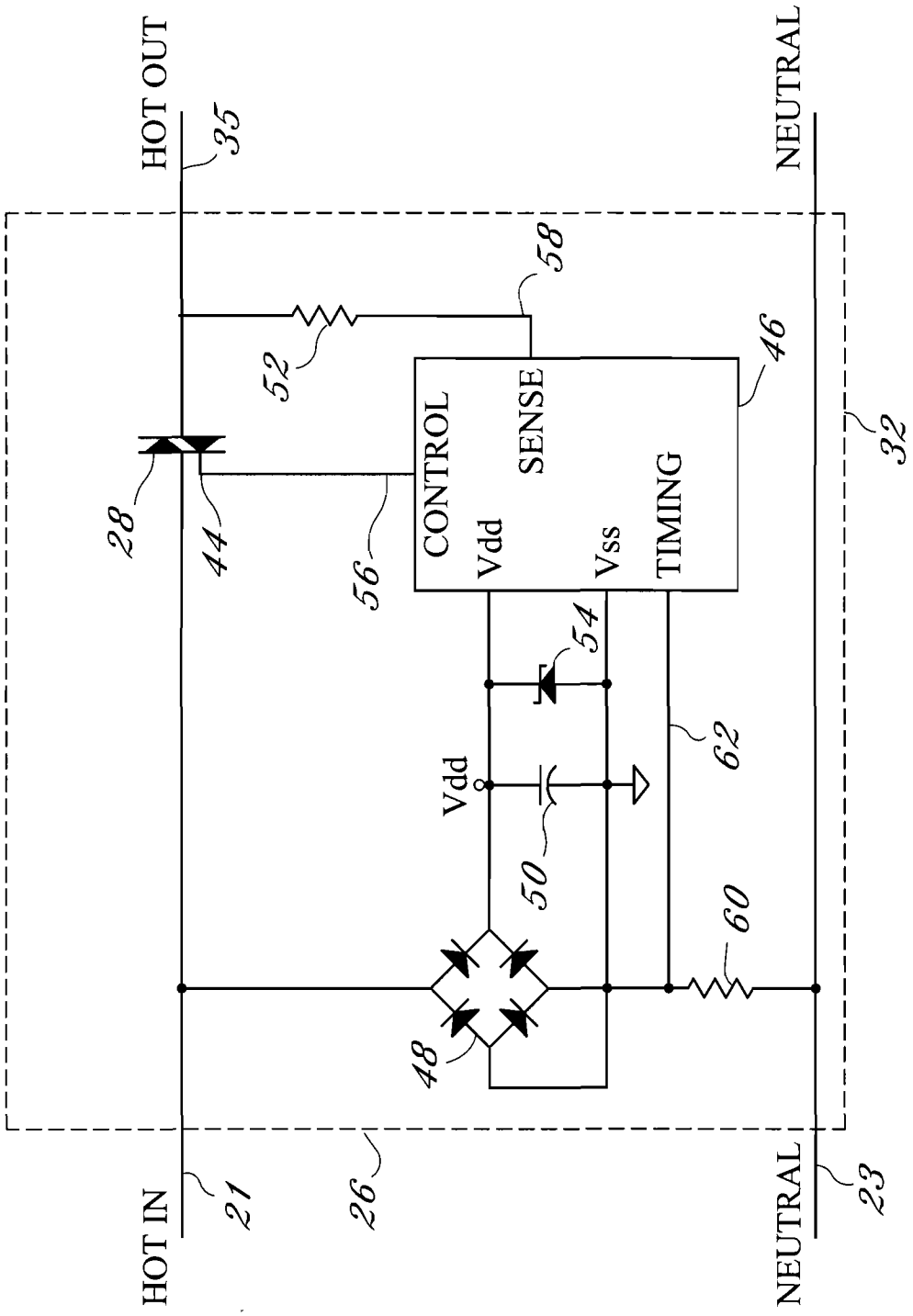

HOT OUT
35
NEUTRAL
58
52
46
28
44
56
CONTROL
SENSE
Vdd
Vss
TIMING
32
54
62
Vdd
50
60
48
HOT IN
21
26
NEUTRAL
23

FIG. 3

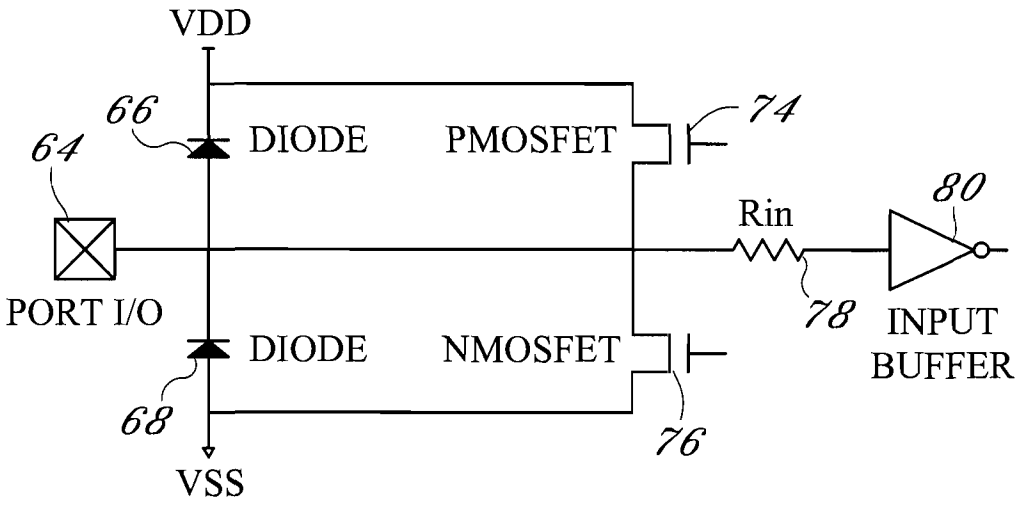

VDD
66
DIODE
PMOSFET
74
64
80
Rin
PORT I/O
78
INPUT
BUFFER
DIODE
NMOSFET
68
76
VSS

FIG. 4

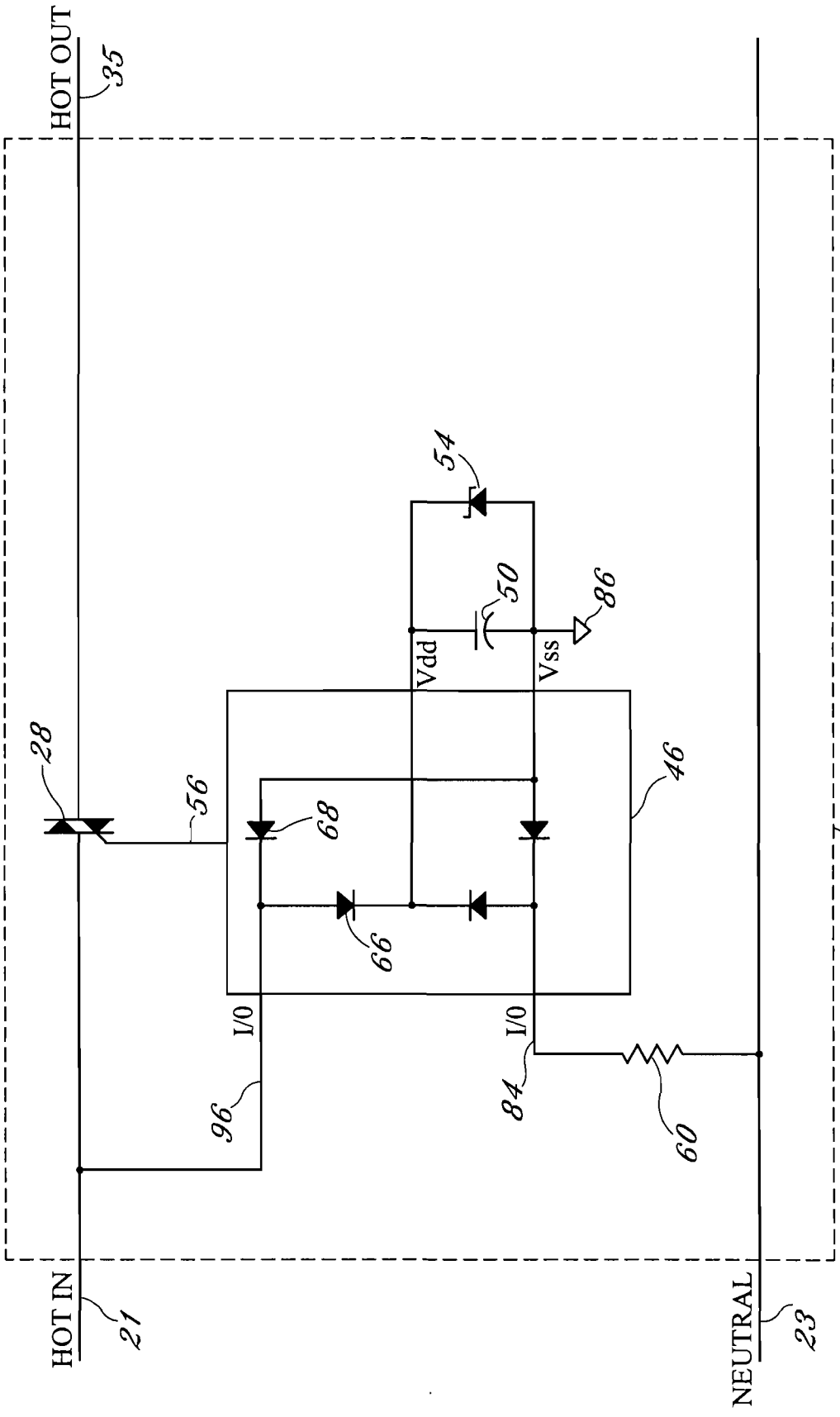

HOT OUT
35
HOT IN
21
NEUTRAL
23
28
56
54
50
86
Vdd
Vss
68
66
46
I/O
I/O
96
84
60
32

FIG. 5

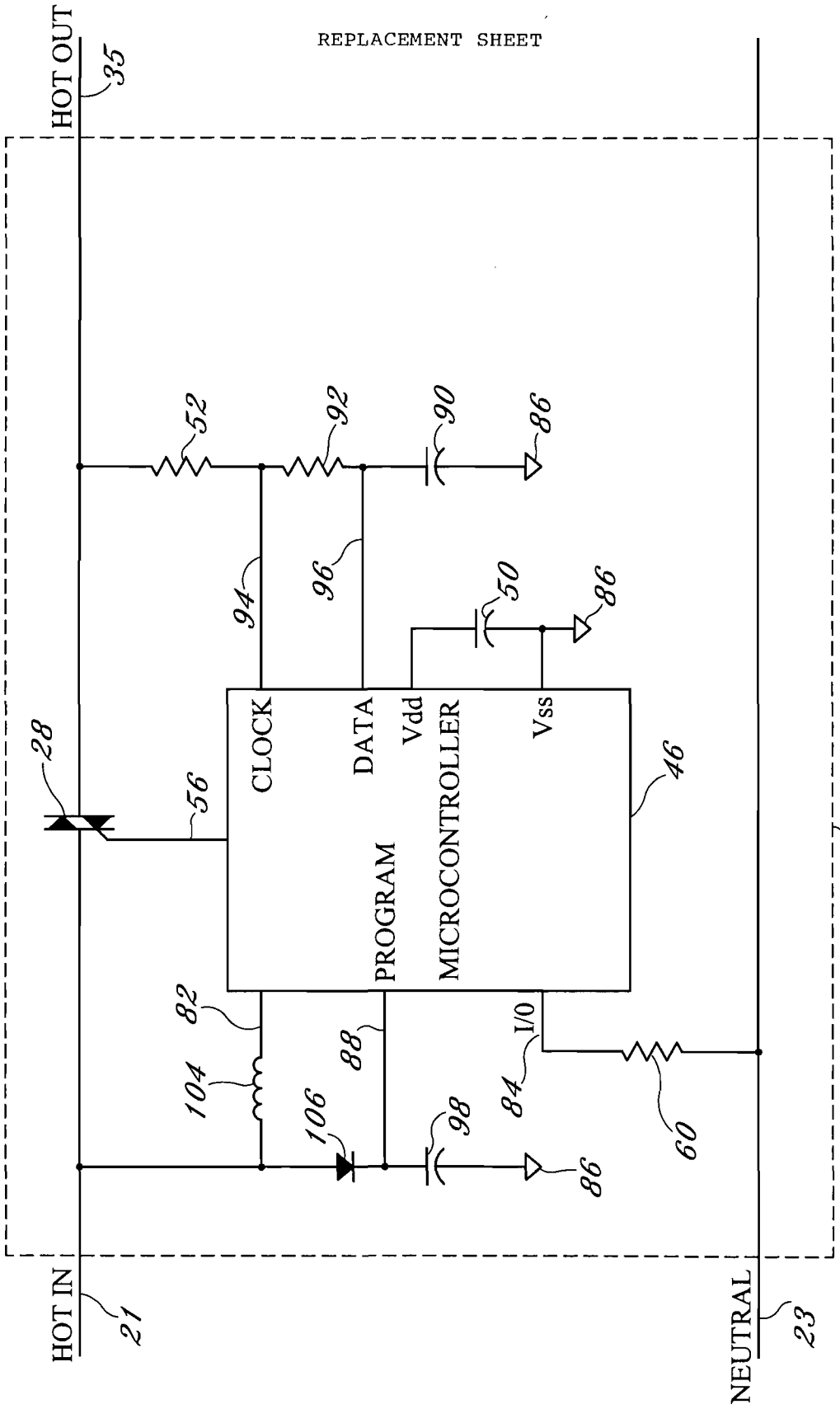

HOT OUT
35
HOT IN
21
NEUTRAL
23
32
52
92
90
86
94
96
50
86
28
56
CLOCK
DATA
Vdd
Vss
46
PROGRAM
MICROCONTROLLER
82
104
88
106
98
86
I/O
84
60

FIG. 6

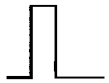
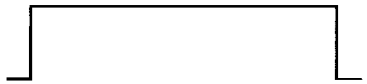
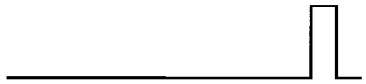
Initial Data State	Desired Data to Be Programmed	Clock Action
Ø	Ø	
Ø	1	
1	Ø	
1	1	

FIG. 7

REPLACEMENT SHEET

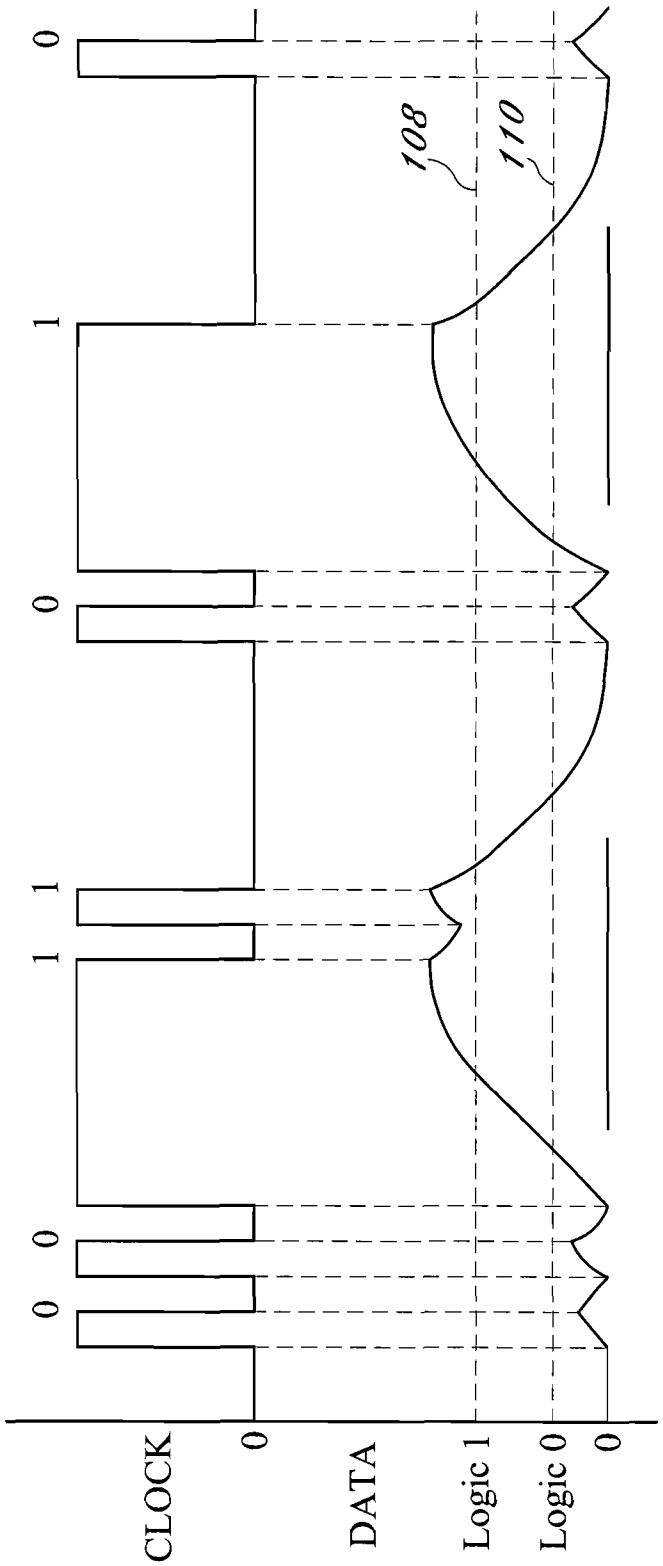


FIG. 8